WHAT IS CLAIMED IS:

1. A transceiver for use within a multi-tier system bus configuration comprising:

means for independently receiving instructions via the system bus from one or more devices connected to the system bus;

means for independently transmitting instructions via the system bus to one or more devices connected to the system bus.

means for buffering instructions received via the system bus; and

means for buffering instructions transmitted via the system bus;

wherein said means for independently receiving instructions is configured to discriminate between different types of input; and

wherein said means for independently transmitting is configured to interleave instructions.

2. The transceiver of claim 1, wherein said means for transmitting is configured to interleave instructions based upon instruction type.

3. The transceiver of claim 2, wherein said instructions are contained within packets and said means for transmitting is configured to interleave instructions based upon packet type.

4. The transceiver of claim 3, wherein said packets comprise direct memory access (DMA) and control action (CA) packet types.

5. The transceiver of claim 1, wherein said means for receiving is configured to discriminate between different types of input based upon received instruction type.

6. The transceiver of claim 5, wherein said input is contained within packets and said means for receiving is configured to discriminate between different types of input based upon packet type.

7. The transceiver of claim 6, wherein said packets comprise direct memory access (DMA) packets and Control Action (CA) packet types.

8. The transceiver of claim 1, wherein said means for receiving is configured to provide specialized control functions.

9. The transceiver of claim 8, wherein said specialized control functions include: a reset function, a timer function, and a broadcast function.